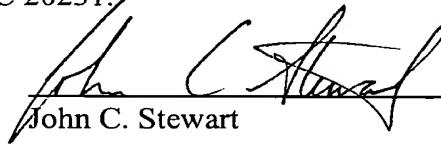


PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, DC 20231.

Date

23 November 1998


John C. Stewart

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leland R. Nevill
Application No. : 08/857,100
Filed : May 15, 1997
For : METHOD AND APPARATUS FOR IDENTIFYING
INTEGRATED CIRCUITS

RECEIVED

DEC 04 1998

Examiner : Daniel St. Cyr
Art Unit : 2876
Docket No. : 660073.555
Date : November 23, 1998

GROUP 2100

Assistant Commissioner for Patents
Washington, DC 20231

FILING FORMAL DRAWINGS

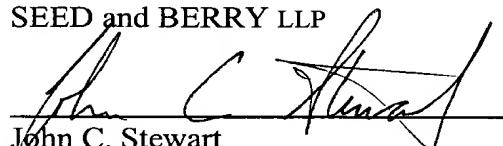
Assistant Commissioner:

Enclosed is a sheet of formal drawings, Figures 1-3, for filing in the above-identified application.

Respectfully submitted,

Leland R. Nevill

SEED and BERRY LLP


John C. Stewart
Registration No. 40,188

JCS:aw

Enclosures:

Postcard

Formal Drawings (1 sheet, Figs. 1-3)

6300 Columbia Center
701 Fifth Avenue
Seattle, Washington 98104-7092
(206) 622-4900; Fax: (206) 682-6031